

TYPES SN5454, SN54H54, SN54L54, SN54LS54, SN7454, SN74H54, SN74LS54 4-WIDE AND-OR-INVERT GATES

REVISED DECEMBER 1983

- Package Options Include Both Plastic and Ceramic Chip Carriers in Addition to Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

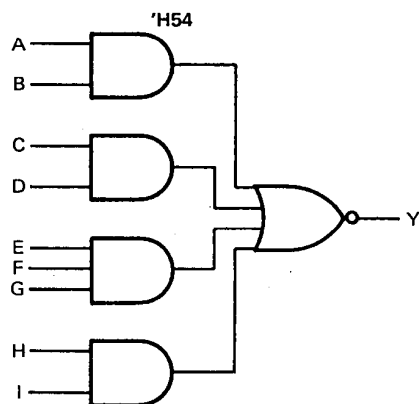
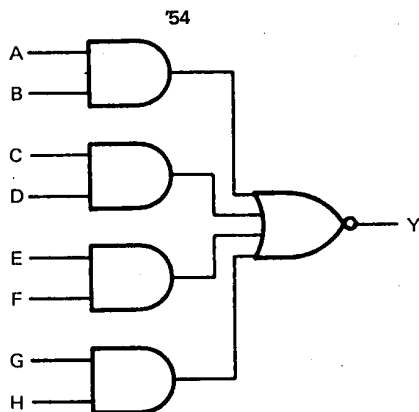
description

These devices contain 4-wide AND-OR-INVERT gates. They perform the following Boolean functions:

$$\begin{aligned} \text{'54 } Y &= \overline{AB + CD + EF + GH} \\ \text{'H54 } Y &= \overline{AB + CD + EFG + HI} \\ \text{'L54, LS54 } Y &= \overline{AB + CDE + FGH + IJ} \end{aligned}$$

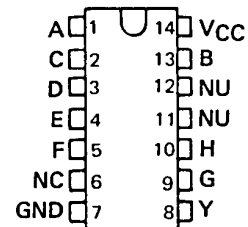
The SN5454, SN54H54, SN54L54, and the SN54LS54 are characterized for operation over the full military temperature range of -55°C to 125°C . The SN7454, SN74H54, and the SN74LS54 are characterized for operation from 0°C to 70°C .

logic diagrams



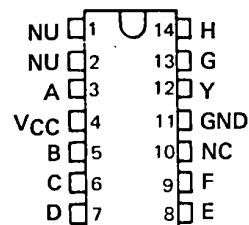
SN5454 . . . J PACKAGE
SN7454 . . . J OR N PACKAGE

(TOP VIEW)



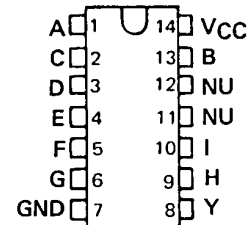
SN5454 . . . W PACKAGE

(TOP VIEW)



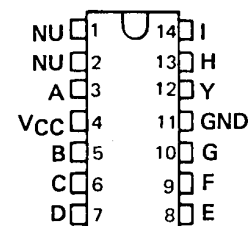
SN54H54 . . . J PACKAGE
SN74H54 . . . J OR N PACKAGE

(TOP VIEW)



SN54H54 . . . W PACKAGE

(TOP VIEW)



NC - No internal connection
NU - Make no external connection

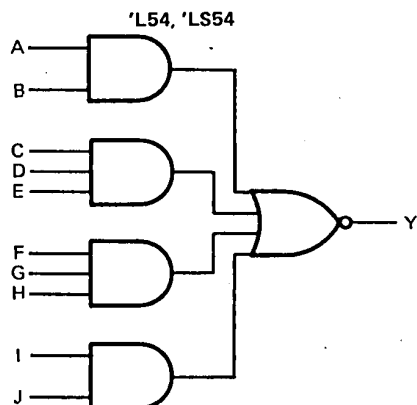
PRODUCTION DATA

This document contains information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

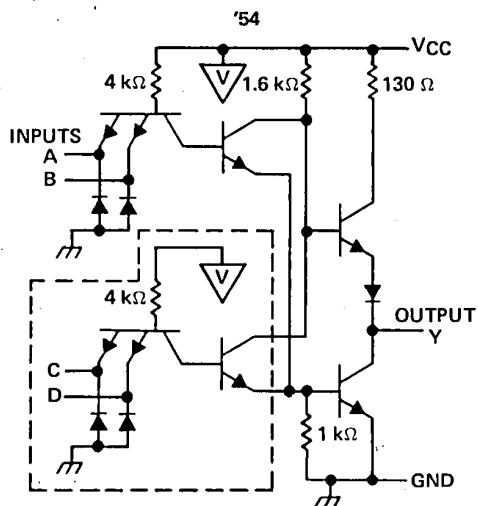
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TYPES SN5454, SN54H54, SN54L54, SN54LS54, SN7454, SN74H54, SN74LS54 4-WIDE AND-OR-INVERT GATES

logic diagrams (continued)



schematics

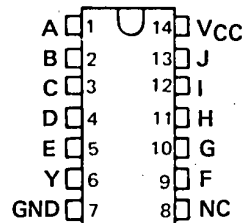


Resistor values shown are nominal.

The portion of the circuits within the dashed lines is repeated for each additional AND section.

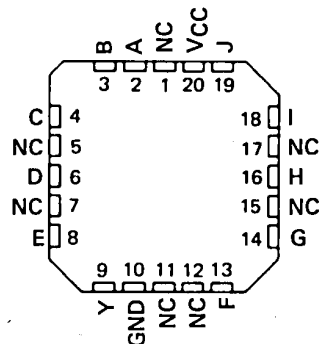
SN54L54 ... J PACKAGE
SN54LS54 ... J OR W PACKAGE
SN74LS54 ... D, J OR N PACKAGE

(TOP VIEW)



SN54LS54 ... FK PACKAGE
SN74LS54 ... FN PACKAGE

(TOP VIEW)

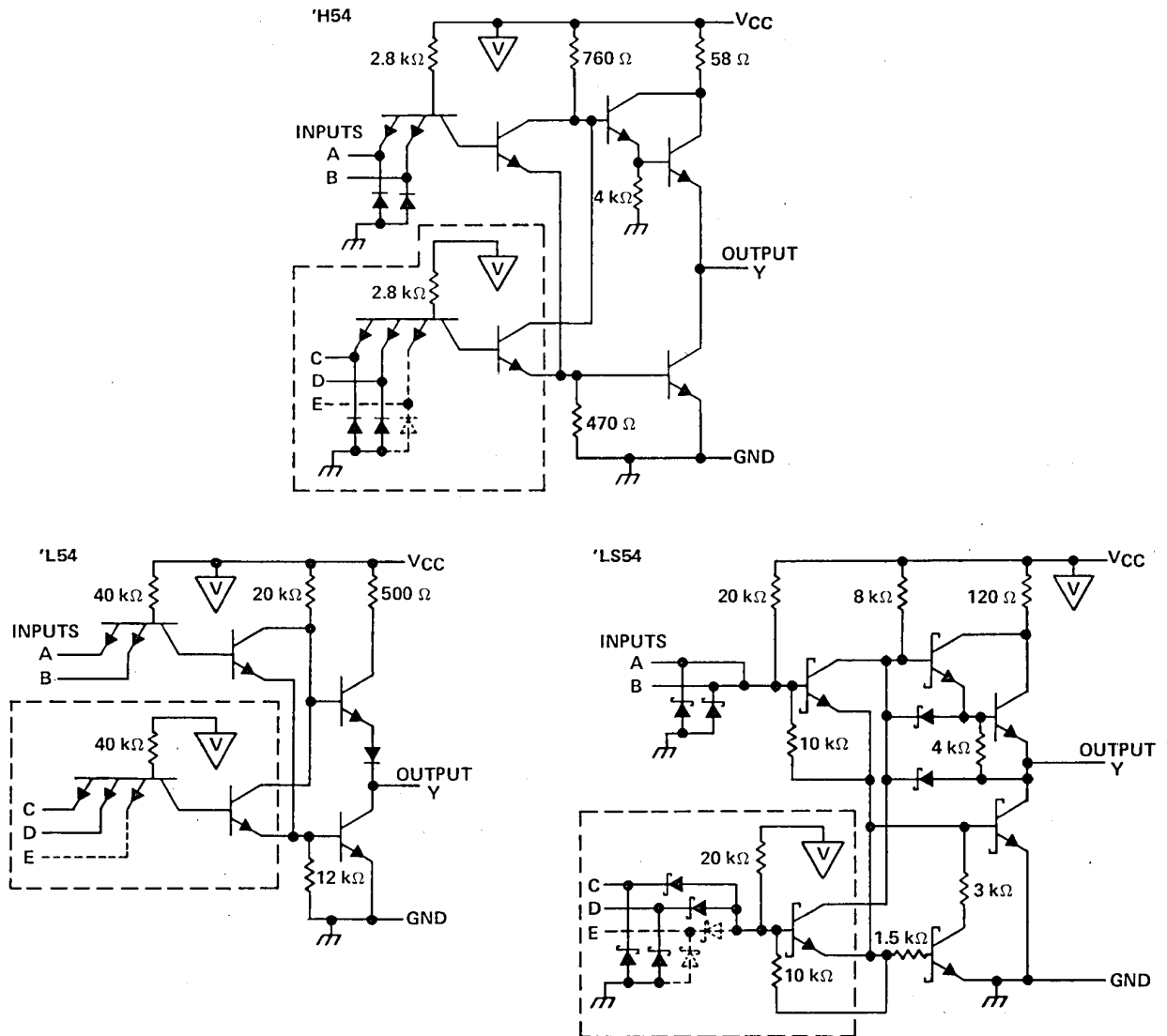


NC - No internal connection

NU - Make no external connection

**TYPES SN5454, SN54H54, SN54L54, SN54LS54,
SN7454, SN74H54, SN74LS54
4-WIDE AND-OR-INVERT GATES**

schematics (continued)



Resistor values shown are nominal.

In 'L54 and 'LS54 circuits, 3-input gate represented by additional dashed line.

The portion of the circuits within the dashed lines is repeated for each additional AND section.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V_{CC} (see Note 1): '54, 'H54, 'LS54	7 V
'L54	8 V
Input voltage: '54, 'H54, 'L54	5.5 V
'LS54	7 V
Operating free-air temperature range: SN54'	-55°C to 125°C
SN74'	0°C to 70°C
Storage temperature range	-65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.

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TTL DEVICES

TYPES SN5454, SN7454

4-WIDE AND-OR-INVERT GATES

recommended operating conditions

	SN5454			SN7454			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V_{IH} High-level input voltage	2			2			V
V_{IL} Low-level input voltage			0.8			0.8	V
I_{OH} High-level output current			-0.4			-0.4	mA
I_{OL} Low-level output current			16			16	mA
T_A Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	SN5454			SN7454			UNIT
		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
V_{IK}	$V_{CC} = \text{MIN}, I_I = -12 \text{ mA}$			-1.5			-1.5	V
V_{OH}	$V_{CC} = \text{MIN}, V_{IL} = 0.8 \text{ V}, I_{OH} = -0.4 \text{ mA}$	2.4	3.4		2.4	3.4		V
V_{OL}	$V_{CC} = \text{MIN}, V_{IH} = 2 \text{ V}, I_{OL} = 16 \text{ mA}$		0.2	0.4		0.2	0.4	V
I_I	$V_{CC} = \text{MAX}, V_I = 5.5 \text{ V}$			1			1	mA
I_{IH}	$V_{CC} = \text{MAX}, V_I = 2.4 \text{ V}$			40			40	µA
I_{IL}	$V_{CC} = \text{MAX}, V_I = 0.4 \text{ V}$			-1.6			-1.6	mA
$I_{OS}§$	$V_{CC} = \text{MAX}$	-20		-55	-18		-55	mA
I_{CCH}	$V_{CC} = \text{MAX}, V_I = 0 \text{ V}$		4	8		4	8	mA
I_{CCL}	$V_{CC} = \text{MAX}, \text{ See Note 2}$		5.1	9.5		5.1	9.5	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$.

§ Not more than one output should be shorted at a time.

NOTE 2: All inputs of one AND gate at 4.5 V, all others at GND.

switching characteristics, $V_{CC} = 5 \text{ V}, T_A = 25^\circ\text{C}$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{PLH}	Any	Y	$R_L = 400 \Omega, C_L = 15 \text{ pF}$		13	22	ns
t_{PHL}					8	15	ns

NOTE 3: See General Information Section for load circuits and voltage waveforms.

TYPES SN54H54, SN74H54 4-WIDE AND-OR-INVERT GATES

recommended operating conditions

	SN54H54			SN74H54			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH} High-level input voltage	2			2			V
V _{IL} Low-level input voltage			0.8			0.8	V
I _{OH} High-level output current			-0.5			-0.5	mA
I _{OL} Low-level output current			20			20	mA
T _A Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS†			MIN	TYP‡	MAX	UNIT
V _{IK}	V _{CC} = MIN,	I _I = -8 mA				-1.5	V
V _{OH}	V _{CC} = MIN,	V _{IL} = 0.8 V, I _{OH} = -0.5 mA		2.4	3.4		V
V _{OL}	V _{CC} = MIN,	V _{IH} = 2 V, I _{OL} = 20 mA			0.2	0.4	V
I _I	V _{CC} = MAX,	V _I = 5.5 V				1	mA
I _{IH}	V _{CC} = MAX,	V _I = 2.4 V				50	µA
I _{IL}	V _{CC} = MAX,	V _I = 0.4 V				-2	mA
I _{OS§}	V _{CC} = MAX			-40		-100	mA
I _{CCH}	V _{CC} = MAX,	V _I = 0 V			7.1	11	mA
I _{CCL}	V _{CC} = MAX,	See Note 2			9.4	14	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V_{CC} = 5 V, T_A = 25°C.

§ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

NOTE 2: An inputs of one AND gate at 4.5 V, all others at GND.

switching characteristics, V_{CC} = 5 V, T_A = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _{PLH}	Any	Y	R _L = 280 Ω, C _L = 25 pF		7	11	ns
t _{PHL}					6.2	11	ns

NOTE 3: See General Information Section for load circuits and voltage waveforms.

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TTL DEVICES

TYPE SN54L54

4-WIDE AND-OR-INVERT GATES

recommended operating conditions

	SN54L54			UNIT
	MIN	NOM	MAX	
V_{CC} Supply voltage	4.5	5	5.5	V
V_{IH} High-level input voltage	2			V
V_{IL} Low-level input voltage			0.7	V
I_{OH} High-level output current			-0.1	mA
I_{OL} Low-level output current			2	mA
T_A Operating free-air temperature	-55		125	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	SN54L54			UNIT
		MIN	TYP‡	MAX	
V_{OH}	$V_{CC} = \text{MIN}$, $V_{IL} = 0.7 \text{ V}$, $I_{OH} = -0.1 \text{ mA}$	2.4	3.3		V
V_{OL}	$V_{CC} = \text{MIN}$, $V_{IH} = 2 \text{ V}$, $I_{OL} = 2 \text{ mA}$		0.15	0.3	V
I_I	$V_{CC} = \text{MAX}$, $V_I = 5.5 \text{ V}$			0.1	mA
I_{IH}	$V_{CC} = \text{MAX}$, $V_I = 2.4 \text{ V}$			10	µA
I_{IL}	$V_{CC} = \text{MAX}$, $V_I = 0.3 \text{ V}$			-0.18	mA
$I_{OS}§$	$V_{CC} = \text{MAX}$	-3		-15	mA
I_{CCH}	$V_{CC} = \text{MAX}$, $V_I = 0 \text{ V}$		0.39	0.8	mA
I_{CCL}	$V_{CC} = \text{MAX}$, See Note 2		0.60	0.9	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$.

§ Not more than one output should be shorted at a time.

NOTE 2: All inputs of one AND gate at 4.5 V, all others at GND.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$ (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{PLH}	Any	Y	$R_L = 4 \text{ k}\Omega$, $C_L = 50 \text{ pF}$		50	90	ns
t_{PHL}					35	60	ns

NOTE 3: See General Information Section for load circuits and voltage waveforms.

TYPES SN54LS54, SN74LS54 4-WIDE AND-OR-INVERT GATES

recommended operating conditions

	SN54LS54			SN74LS54			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH} High-level input voltage	2			2			V
V _{IL} Low-level input voltage			0.7			0.8	V
I _{OH} High-level output current			-0.4			-0.4	mA
I _{OL} Low-level output current			4			8	mA
T _A Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	SN54LS54			SN74LS54			UNIT
		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
V _{IK}	V _{CC} = MIN, I _I = -18 mA			-1.5			-1.5	V
V _{OH}	V _{CC} = MIN, V _{IL} = MAX, I _{OH} = -0.4 mA	2.5	3.4		2.7	3.4		V
V _{OL}	V _{CC} = MIN, V _{IH} = 2 V, I _{OL} = 4 mA	0.25	0.4		0.25	0.4		V
	V _{CC} = MIN, V _{IH} = 2 V, I _{OL} = 8 mA				0.35	0.5		
I _I	V _{CC} = MAX, V _I = 7 V			0.1			0.1	mA
I _{IH}	V _{CC} = MAX, V _I = 2.7 V			20			20	µA
I _{IL}	V _{CC} = MAX, V _I = 0.4 V			-0.4			-0.4	mA
I _{OS} §	V _{CC} = MAX	-20		-100	-20		-100	mA
I _{CCH}	V _{CC} = MAX, V _I = 0 V		0.8	1.6		0.8	1.6	mA
I _{CCL}	V _{CC} = MAX, See Note 2		1	2		1	2	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at V_{CC} = 5 V, T_A = 25°C.

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

NOTE 2: All inputs of one AND gate at 4.5 V, all others at GND.

switching characteristics, V_{CC} = 5 V, T_A = 25°C (see note 3)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _{PLH}	Any	Y	R _L = 2 kΩ, C _L = 15 pF		12	20	ns
t _{PHL}					12.5	20	ns

NOTE 3: See General Information Section for load circuits and voltage waveforms.



TTL DEVICES

